

Electronic Patent Application Fee Transmittal

Application Number:	10759936			
Filing Date:	16-Jan-2004			
Title of Invention:	Method and apparatus for VLSI clock gated power estimation using LCB counts			
First Named Inventor/Applicant Name:	Rajat Chaudhry			
Filer:	Gregory W. Carr/Bradley Ellis			
Attorney Docket Number:	AUS920030974US1			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Filing a brief in support of an appeal	1402	1	510	510
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				510